549,678

(54) Title: METHOD AND APPARATUS FOR PARTITIONING ALLOCATION AND MANAGEMENT OF JITTER BUFFER
MEMORY FOR TDM CIRCUIT EMULATION APPLICATIONS

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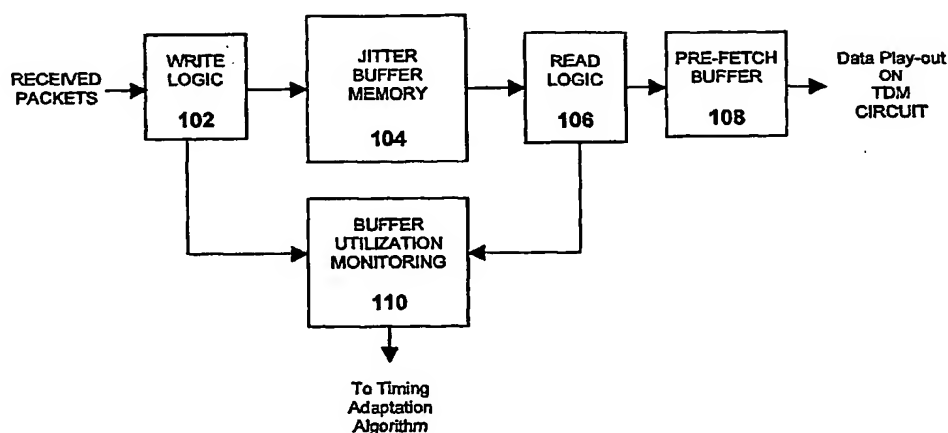
(57) Abstract: A method for partitioning allocation and management of a jitter buffer memory for TDM circuit emulation applica-
tions comprises the steps of obtaining a channel hierarchy for a plurality of packet carrying channels having different channel rates,
obtaining a packet sequential number, and using the channel hierarchy and the packet sequential number, generating a base-address
in the jitter buffer memory. Each channel is allocated a space in the buffer memory that is proportional to its rate, and out-of-order
packets are automatically reordered by the jitter buffer.